

Low Noise Devices Typical Specifications @ TA = 25°C

Part Number	Gate Length (μm)	Gate Width (μm)	Recommended Frequency Range (GHz)	Test Frequency (GHz)	NF/GA Bias		NF _{OPT} (dB)	GA (dB)	Power Bias		P _{1dB} (dBm)	Chip / Package Number	Chip / Package Description	Available Screening Grades
					V _{DS} (V)	I _{DS} (mA)			V _{DS} (V)	I _{DS} (mA)				
NE27200	0.2	200	0.1 to 40	12	2.0	10	0.45	13.0	3.0	30	12.0	00	Chip	L or B
NE67400	0.3	280	2.0 to 18	12	3.0	10	1.4	10.0	3.0	30	14.5	00	Chip	L or B
NE321000	0.2	160	0.1 to 18	12	2.0	10	0.35	13.5	—	—	—	00	Chip	D
NE32500	0.2	200	0.1 to 40	12	2.0	10	0.45	13.0	3.0	30	12.0	00	Chip	D
NE3210S01	0.2	160	2 to 18	12	2.0	10	0.35	13.5	—	—	—	S01	Plastic SMD	D
NE34018	0.6	400	0.1 to 6	2	2.0	5	0.6	16.0	3.0	30	16.0	18	Plastic SMD	D
NE4210S01	0.2	160	2 to 18	12	2.0	10	0.50	13.0	—	—	—	S01	Plastic SMD	D
NE67483B	0.3	280	2.0 to 18	12	3.0	10	1.4	10.0	3.0	30	14.5	83B	Plastic SMD	L or B

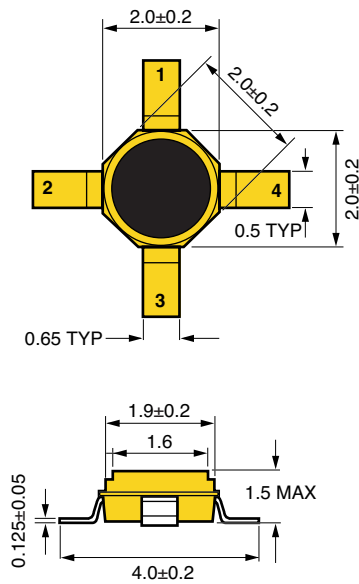
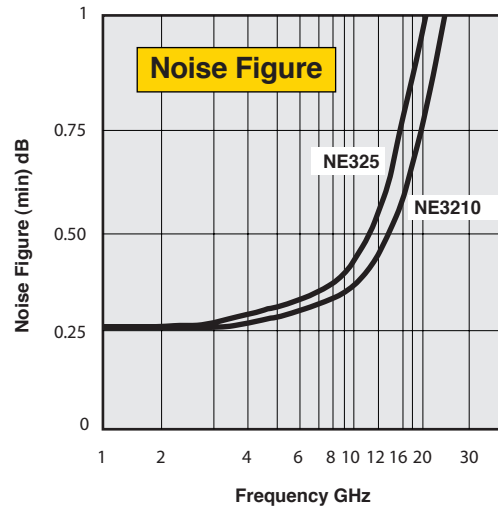
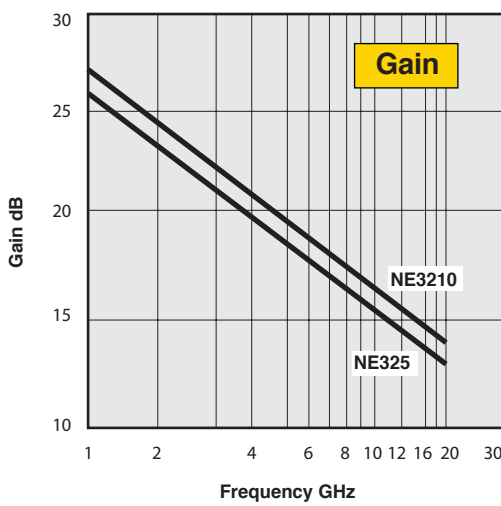
Note: All devices except chips are available on tape and reel.

General Purpose and HBT Devices Typical Specifications @ TA = 25°C

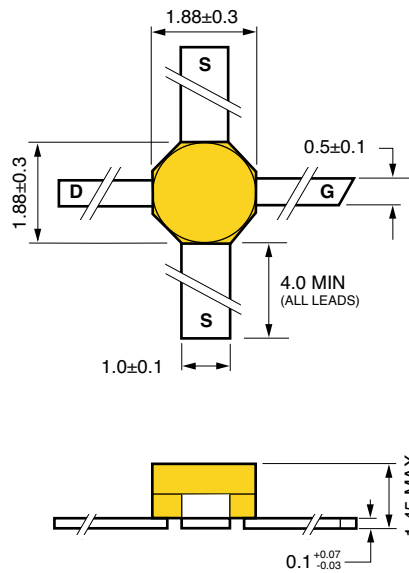
Part Number	Gate Length (μm)	Gate Width (μm)	Recommended Frequency Range (GHz)	Test Frequency (GHz)	NF/GA Bias		NF _{OPT} (dB)	GA (dB)	Power Bias		P _{1dB} (dBm)	Chip / Package Number	Chip / Package Description	Available Screening Grades
					V _{DS} (V)	I _{DS} (mA)			V _{DS} (V)	I _{DS} (mA)				
NE67400	0.3	280	2.0 to 18	12	3.0	10	1.4	10.0	3.0	30	14.5	00	Chip	L or B
NE34018	0.6	400	2 to 14	2	2.0	5	0.6	16.0	3.0	30	16.0	18	Plastic SMD	D
NE52418	—	—	0.1 to 14	2	2.0	3	1.0	16.0	2.8	30	14.0	18	Plastic SMD	D
NE67483B	0.3	280	2.0 to 18	12	3.0	10	1.4	10.0	3.0	30	14.5	83B	Hermetic Metal Ceramic	L or B

Note: All devices except chips are available on tape and reel.

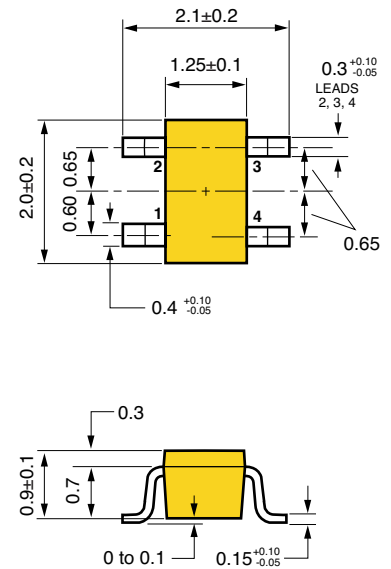
Performance NE3210 vs NE325



S01 Package



83B Package



18 Package

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